

INSITUTE OF ENGINEERING & TECHNOLOGY
DEVI AHILYA UNIVERSITY
B. E. II YEAR (ELECTRONICS AND TELECOMMUNICATION- SEC. A)
PRACTICAL EXAM TIME TABLE, NOV.-DEC., 2017

DATE	SUB CODE	SUB NAME/TITLE NAME	INTERNAL EXAMINER
25-11-2017	ETR3C2/ ETR4C4/2ET502/2ET553	Digital Electronics/ Analog Communication/ Microprocessors & Assembly Language Programming	Dr. Vaibhav Neema
27-11-2017	ETR3C3	Data Structure	Mr. Vikas Wankhede
28-11-2017	ETR3C4/ ETR4C2/2ET503/2ET504	Network Analysis / Electrical And Electronic Measurement/ Elec.& Elex. Measurement/ Circuit Analysis & Synthesis	Dr. Ajay Verma
29-11-2017	ETR3L1/ ETR4L2/2ET505/2ET556	Electronic Workshop I/ Electronic Workshop II/ Microelectronics	Dr. Vaibhav Neema
30-11-2017	ETR3V3/ETR4V4	Comprehensive Viva (Sec A- B1)	Dr. S. Tokekar/ Dr. R. Upadhyay
30-11-2017	ETR3V3/ETR4V4	Comprehensive Viva (Sec A- B2)	Dr. Shashi Prakash/ Dr. V. Neema

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DATE	SUB CODE	SUB NAME/TITLE NAME	INTERNAL EXAMINER
25-11-2017	ETR3C2/ ETR4C4/2ET554	Digital Electronics/ Analog Communication/ Electromagnetic Field And Transmission Lines	Dr. Ravi Sindal
27-11-2017	ETR3L1/ ETR4L2	Electronic Workshop I/ Electronic Workshop II	Dr. Ravi Sindal
28-11-2017	ETR3C3	Data Structure	Mr. Vikas Wankhede
29-11-2017	ETR3C4/ETR4C3	Network Analysis/ Analog Electronics	Dr. D. Nitnaware
29-11-2017	ETR3V3/ETR4V4	Comprehensive Viva (Sec B- B1)	Dr. Ajay Verma/ Dr. D. Nitnaware
30-11-2017	ETR3V3/ETR4V4	Comprehensive Viva (Sec B- B2)	Dr Ravi Sindal/Mr. P. D Ashok

Sr. Supdt.

Exam, Nov.-Dec. 2017

Sr. Supdt. of Exam.
Institute of Engineering & Technology
Devi Ahilya Vishwavidyalaya
New Campus, Khandwa Road,
INDORE (M.P.) 452017