

**Devi Ahilya Vishwavidyalaya, Indore**  
**Institute of Engineering and Technology**  
Session Jan-May 2018  
**Class Test-II Time Table**  
**MARCH-2018**  
**M.E. I year (Digital Communication) (Full Time)**

S.N.	Date	Time	Sub Code	Sub. Name
1.	08/03/2018	11.00AM To 12:10 PM.	DCR2C1	Modeling And Simulation
2.	08/03/2018	02.00PM To 03:10 PM.	DCR2C2	Mobile Comm. System
3.	09/03/2018	11.00AM To 12:10 PM.	DCR2C3	System Design Using Verilog
4.	09/03/2018	02.00PM To 03:10 PM.	DCR2G1	Broadband Access Tech.
5.	10/03/2018	11.00AM To 12:10 PM.	DCR2E1	Analog and Digital CMOS Circuit Design
6.	10/03/2018	02.00PM To 03:10 PM.	ASR2S2	Soft Skills II



**Sr. Supdt. (Exam)**  
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