

Devi Ahilya Vishwavidyalaya, Indore
Institute of Engineering and Technology
Session Jan-May 2017
Class Test-III Time Table
April-2017

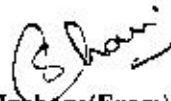
M.E. I year (Digital Instrumentation Engg.) (Full Time)

S.N.	Sub. name	Sub Code	Date	Time
1.	Modeling and Simulation(GE-II)	DIR2G3	06/04/2017	2:00-3:10PM
2.	Process Instt. And Industrial Control	DIR2C2	06/04/2017	4:00-5:10PM
3.	System Design Using Verilog	DIR2C3	07/04/2017	2:00-3:10PM
4.	Digital Image Processing	DIR2C1	07/04/2017	4:00-5:10PM
5.	Analog and Digital CMOS Circuit Design	DIR2E1	08/04/2017	2:00-3:10PM
6.	Soft Skills II	ASR2S2	08/04/2017	4:00-5:10PM

Devi Ahilya Vishwavidyalaya, Indore
Institute of Engineering and Technology
Session Jan-May 2017
Class Test-II Time Table
March-2017

M.E. I year (Digital Communication) (Full Time)

S.N.	Sub. Name	Sub Code	Date	Time
1.	Modeling And Simulation	DCR2C1	06/04/2017	2:00-3:10PM
2.	Mobile Comm. System	DCR2C2	06/04/2017	4:00-5:10PM
3.	System Design Using Verilog	DCR2C3	07/04/2017	2:00-3:10PM
4.	Broadband Access Tech.	DCR2G1	07/04/2017	4:00-5:10PM
5.	Analog and Digital CMOS Circuit Design	DCR2E1	08/04/2017	2:00-3:10PM
6.	Soft Skills II	ASR2S2	08/04/2017	4:00-5:10PM


Prof. Incharge(Exam)

Sr. Supdt. of Exam.
Institute of Engineering & Technology
Devi Ahilya Vishwavidyalaya
New Campus, Khandwa Road,
INDORE (M.P.) 452017