

**Devi Ahilya University**  
**Institute of Engineering & Technology, Indore**  
**Class Test II**  
**Time Table – MARCH, 2018**

**II E&TC Engineering (Section A & B)**

S. No	Date	Time	Sub. Code	Sub. Name
1	08/03/2018	11.00AM To 12:10	ETR4C1	Computer Orgination & Architecture
2	08/03/2018	02.00PM To 03:10	ETR4C2	Electrical & Electronics Measurement
3	09/03/2018	11.00AM To 12:10	ETR4C3	Analog Electronics
4	09/03/2018	02.00PM To 03:10	ETR4C4	Analog Communication
5	10/03/2018	11.00AM To 12:10	ETR4G2	Signal & System
6	10/03/2018	02.00PM To 03:10	STR4S4	Engineering Economics

**III E&TC Engineering(OLD SCHEME)**

S. No	Date	Time	Sub. Code	Sub. Name
1	08/03/2018	11.00AM To 12:10	3ET552	Telecom Networks
2	08/03/2018	02.00PM To 03:10	3ET553	Object Oriented System
3	09/03/2018	11.00AM To 12:10	3ET555	Control System
4	09/03/2018	02.00PM To 03:10	3ET554	Digital Signal Processing
5	10/03/2018	11.00AM To 12:10	3EI558	Operating System
6	10/03/2018	02.00PM To 03:10	3ET557	Entrepreneurship Dev & IPR

**III E&TC Engineering(CBCS) (Section A & B)**

S. No	Date	Time	Sub. Code	Sub. Name
1	08/03/2018	11.00AM To 12:10	ETR6C1	VLSI Design
2	08/03/2018	02.00PM To 03:10	ETR6C2	Mobile & Wireless Communication
3	09/03/2018	11.00AM To 12:10	ETR6C3	Control System
4	09/03/2018	02.00PM To 03:10	ETR6E1	Linear Intrigrated Circuit
5	10/03/2018	11.00AM To 12:10	ETR6G4	Computer Network
6	10/03/2018	02.00PM To 03:10	STR6S6	Entrepreneurship Dev & IPR

**IV E&TC Engineering (Section A & B)**

S. No	Date	Time	Sub. Code	Sub. Name
1	08/03/2018	11.00AM To 12:10	4ET551	Circuit Design Using HDL
2	08/03/2018	02.00PM To 03:10	4ET552	Optical Communication
3	09/03/2018	11.00AM To 12:10	4ET553	IC Design Technique
4	09/03/2018	02.00PM To 03:10	4ET554	Satellite Communication & Navigation

  
**Sr. Supdt. (Exam)**

**Sr. Supdt. of Exam.**  
 Institute of Engineering & Technology  
 Devi Ahilya Vishwavidyalaya  
 New Campus, Khandwa Road,  
 INDORE (M.P.) 452017