

Devi Ahilya University, Indore, India Institute of Engineering & Technology				II Year B.E. (Electronics and Telecommunication Engg.)			
Subject Code & Name	Instructions Hours per Week			Credits			
4ETRC3 LINEAR DEVICES AND APPLICATIONS	L	T	P	L	T	P	Total
	3	1	2	3	1	1	5
Duration of Theory Paper: 3 Hours							

Learning Objectives:

In-depth knowledge of Operational Amplifier and IC555 including its circuit analysis, design and application

Prerequisites:

Analog Electronics

COURSE CONTENTS

Unit 1: Fundamentals of operational amplifier

Operational Amplifier, Equivalent Circuit, Circuit symbols and Terminals. Op-Amp IC 741 pin diagram and pin function, Op-Amp parameters Input offset voltage, Input Offset current, Input bias current, Differential input resistance, Input capacitance, input voltage range, offset voltage adjustment range, Common Mode Rejection Ratio (CMRR), Supply Voltage Rejection Ratio (SVRR), Slew Rate, Large Signal Voltage Gain, Supply voltage, Supply Current, Output voltage Swing, Gain Bandwidth Product, Output Short Circuit Current, Transfer Characteristic- Ideal and Practical Voltage Transfer Curve, Op-Amp Configuration: Open Loop and Closed loop, Virtual Ground Concept, Features, pin diagram and pin function of dual Op Amp IC 747.

Unit 2: Basic OP-Amp Circuits

Closed Loop configuration, modes of operations: Inverting and Non- Inverting modes, Differential amplifier, Unity Gain Amplifier (voltage follower), Arithmetic operations: Addition, multiplication, Scaling, Averaging, Subtraction, Integrator, Differentiator, Concept of frequency compensation of Op-Amp and offset nulling

Unit 3: OP-Amp Applications

Op-Amp as an Instrumentation amplifier: Working, Derivation of output voltage, IC LM 324- Pin Configuration, specification and application, Voltage to Current converter with Floating and Grounded load, Current to Voltage converter, Sample and Hold Circuit, Logarithmic and Antilogarithmic amplifier using diodes, Analog Divider and analog multiplier, Comparators: IC LM710, Zero Crossing Detector, Schmitt Trigger, Window Detector, Phase Detector Active Peak Detector, Peak to Peak Detector.

Unit 4: Filters and Oscillators

Filter and its classification, Merits and demerits of active filters over passive filters, Filter characteristic terms: order of filter, cut off frequency. Pass band. Stop band, Centre frequency, Q factor, Filter types and its Frequency Response: Low pass(First Order and second order).High Pass (First Order and second order).Band pass(Wide and Narrow), Band Reject(Wide and Narrow), All Pass Filter, Oscillator types using IC 741: Phase shift oscillator, Wein Bridge oscillator, Colpitts oscillator, Hartley oscillator.

Unit 5: IC 555 and other Linear IC

IC 555: Block Diagram of Timer, Pin diagram and functions, Astable. Monostable, Bistable multivibrator. Schmitt trigger and Voltage Control Oscillator, Phase Lock Loop (PLL): Block diagram and its operation, lock range and capture range, Applications of PLL: PLL as Multiplier, FM Demodulator, IC 565: Pin diagram and function

Learning Outcomes:

Upon completing the course, students will be able to:

- Understand op-amp's basic construction, characteristics, parameter limitations, various configurations and other applications of op-amp.
- Analyze and design basic op-amp circuits, particularly various linear and non-linear circuits, active filters, signal generators, and data converters

BOOKS RECOMMENDED:

- [1] Millman & Halkias - Integrated Electronics, Tata McGraw Hill.
- [2] Franco-Design with Operational Amplifiers & Analog Integrated Circuits, TMH
- [3] Schilling & Belove-Electronic Circuit, Discrete & Integrated , TMH
- [4] Gayakwad R.A- Op-Amps and Linear IC's, Pearson .
- [5] Coughlin and Driscoll – Operational Amplifier and Linear Integrated Circuits – Pearson Education Asia

List of Practical Assignments:

During the learning of course, students need to carryout following assignments:

- 1) To measure the gain of inverting & non-inverting amplifier.
- 2) To analyze the voltage transfer characteristics of op-amp.
- 3) To study the inverting adder & subtractor configuration of op-amp.
- 4) To study the non- inverting adder & subtractor configuration of op-amp.
- 5) To study the differentiator configuration of op-amp.
- 6) To study the integrator configuration of op-amp.
- 7) To study the low pass & high pass filter.
- 8) To study the band pass & band reject filter.
- 9) To study the zero crossing detector.
- 10) To analyze the performance of comparator.