

Devi Ahilya Vishwavidyalaya, Indore
Institute of Engineering & Technology

CLASS TIME TABLE (Session: Dec. 2016- May. 2017) w.e.f- Dated: 06/01/2017

Class & Branch: II Year BE Electronics & Telecommunication (Section-A)

Class Teacher : Mr. Shalendra Pathak

Day	8:00-9:00	9:00-10:00	10:00-11:00	11:00-12:00	12:00-13:00	13:00-14:00	14:00-15:00	15:00-16:00	16:00-17:00
Monday			ETR4C1 SB D-101	ETR4G2 SVT D-101	ETR4G2 SVT D-101	B R E A K	ETR4C3 HP D-101	ETR4C2 SS D-101	ETR4C4 Sh.P D-101
Tuesday			ETR4C1 PR D-101	ETR4C4 Sh.P D-101	ETR4C3 HP D-101		ETR4C2 SS D-101	STR4S4 DA D-101	STR4S4 DA D-101
Wednesday			ETR4C1 SB D-101	ETR4G2 SVT D-101	ETR4G2 SVT D-101		ETR4C3 HP D-101	ETR4L2 SB/NJh D-204 B1	
Thursday				ETR4C2 SS D-101	ETR4C3 HP D-101		ETR4C1 PR D-101	ETR4C4 Sh.P/AM D-209 B2	
								ETR4C2 SS/AG E-101 B2	
Friday			ETR4C3 HP (RMC*)	ETR4C2 SS (RMC*)	ETR4C1 SB (RMC*)	ETR4C2 SS E-101 B1		ETR4C2 SS D-101	ETR4C4 Sh.P D-101
Saturday				ETR4I2 SB/PS D-204 B2		B R E A K	ETR4C3 HP/SKH D-108 B2	ETR4C4 Sh.P (RMC*)	ETR4G2 SVT (RMC*)
				ETR4C3 HP/AG D-108 B1			ETR4C4 Sh.P D-101		
Subject Code	Subject Name	Marks Scheme	Keys.	Faculty Name	Keys.	Faculty Name			
ETR4C1	Computer Orgnization & Arc		SB	Mrs. Shivangi Bande	NJh	Naveen Joshi			
ETR4C2	Electrical & Electronic Meas.		SS	Ms. Sangita Solanki	AM	Ankit Meshram			
ETR4C3	Analog Electronics		HP	Ms. Hemlata Pal	SSH	Saurabh Singh			
ETR4C4	Analog Communication		Sh.P	Mr. Shalendra Pathak	AG	Amit Gautam			
ETR4G2	Signal & Systems		SVT	Dr. Sanjiv Tokekar					
ETR4L2	Electronic Workshop II		SB	Dr. Shivangi Bande					
STR4S4	Engg. Economics		DA	Dr. Deepshikha Agrawal					
			SKH	Mr. Shahid Khilji					
			PS	Ms. Priyanka Sharma					

Note:- Classes will be conducted only in allotted rooms & time, Mutual exchange of lectures is not permitted until the permission of Co-ordinator/HOD/Director

RMC* Remedial classes for weaker and underperformed student