

Devi Ahilya Vishwavidyalaya, Indore
Institute of Engineering & Technology

CLASS TIME TABLE (Session: Dec. 2016- May. 2017) w.e.f- Dated: 06/01/2017

Class & Branch: IV Year BE Electronics & Telecommunication (Section-A)

Class Teacher :Dr. Uma Bhatt

Day	8:00-9:00	9:00-10:00	10:00-11:00	11:00-12:00	12:00-13:00	13:00-14:00	14:00-15:00	15:00-16:00	16:00-17:00
Monday			4ET552 PDA /PT D-204 B1		B R E A K	4ET552 SP D-201	4ET551 RS D-201	4ET551 RS D-201	4ET552 SP (RMC*)
			4ET553 PSN/AD D-206 B2			4ET552 SP D-201	4ET554 VM D-201	4ET553 PSN D-201	
Tuesday			4ET552 PT/NJ D-204 B2			4ET552 SP D-201	4ET554 VM D-201	4ET553 PSN D-201	
			4ET553 PSN/AD D-206 B1			4ET552 SP D-201	4ET554 VM D-201	4ET553 PSN D-201	
Wednesday				4ET551 RS (RMC*)		4ET552 SP D-201	4ET551 RS D-201	4ET551 RS D-201	4ET553 PSN (RMC*)
Thursday			PROJECT PHSE I/II			4ET552 SP D-201	4ET553 PSN D-201	4ET554 UB D-201	4ET554 UB/ALS D-206 B1
			RESPECTIVE GUIDES			4ET552 SP D-201	4ET551 RS D-201	4ET551 RS D-201	4ET553 PSN (RMC*)
Friday						4ET554 UB D-201	4ET553 PSN D-201	4ET554 UB/ALS D-206 B1	
Saturday			4ET553 PSN D-201	4ET554 UB D-201	4ET554 ALS D-206 B2	4ET554 UB (RMC*)			
Subject Code	Subject Name		Marks Scheme		Keys.	Faculty Name		Keys.	Faculty Name
4ET551	Circuit Design Using HDL		(TH-100,CW-50)		RS	Dr. Ravi Sindal		PT	Puneet Thakur
4ET552	Optical Communication		(TH-100,CW-50,PR-50,SW-50)		SP	Dr. Shashi Prakash		NJ	Nitin Jain
4ET553	IC Design Techniques		(TH-100,CW-50,PR-50,SW-50)		PSN	Mr. Praveen Singh		AD	Anjali Dadhich
4ET554	Satellite comm & Navig.		(TH-100,CW-50,PR-50,SW-50)		UB	Dr. Uma Bhatt		VM	Mr. Vijendra Mishra
4ET510 4ET560	Project Phase I/II							ALS	Alpa Sharma
					PDA	Mr. P.D. Ashok			

Note:- Classes will be conducted only in allotted rooms & time, Mutual exchange of lectures is not permitted until the permission of Co-ordinator/HOD/Director

RMC* Remedial classes for weaker and underperformed student