

Devi Ahilya Vishwavidyalaya, Indore  
Institute of Engineering & Technology  
CLASS TIME TABLE (Session: Dec. 2017- May. 2018) w.e.f- Dated: 01/01/2018

Class & Branch: IV Year BE Electronics & Telecommunication (Section-B)

Class Teacher : Mrs. Priyanka Sharma

Day	8:00-9:00	9:00-10:00	10:00-11:00	11:00-12:00	12:00-13:00	13:00-14:00	14:00-15:00	15:00-16:00	16:00-17:00
Monday			4ET552 SP E-301	4ET551 BB E-301	<b>BREAK</b>	4ET552 BB D-204 B2		PROJECT PHSE II RESPECTIVE GUIDE	
						4ET553 PS D-206 B1			
Tuesday			4ET552 SP E-301	4ET551 BB E-301	4ET553 PS E-301	<b>B R E A K</b>	4ET554 UB E-301	PROJECT PHSE II RESPECTIVE GUIDE	
Wednesday			4ET552 SP E-301	4ET551 BB E-301	4ET553 PS E-301		4ET554 AKS E-301	PROJECT PHSE II RESPECTIVE GUIDE	
Thursday			4ET552 SP E-301	4ET551 BB E-301	4ET553 PS E-301		4ET554 AKS E-301	4ET554 UB D-206 B1	
Friday			4ET553 PS E-301	4ET552 SB D-204 B1			4ET554 UB E-301	4ET554 AKS D-206 B2	
				4ET553 PS D-206 B2			4ET554 UB E-301		
Saturday			4ET552 SP RMC* E-301	4ET551 BB RMC*E-301	4ET553 PS RMC* E-301		4ET554 UB RMC* E-301		
Subject Code	Subject Name		Marks Scheme		Keys.	Faculty Name		Keys.	TA Name
4ET551	Circuit Design Using HDL		(TH-100,CW-50)		BB	MR. BS BHALAVI			
4ET552	Optical Communication		(TH-100,CW-50,PR-50,SW-50)		SP	DR. SHASHI PRAKASH		SB	MRS. SHIVANGI BANDE
4ET553	IC Design Techniques		(TH-100,CW-50,PR-50,SW-50)		PS	MRS. PRIYANKA SHARMA			
4ET554	Satellite comm & Navig.		(TH-100,CW-50,PR-50,SW-50)		UB	DR. UMA BHATT		AKS	MR. ANKIT SONI
4ET510 4ET560	Project Phase-I/II								

Note:- Classes will be conducted only in allotted rooms & time, Mutual exchange of lectures is not permitted until the permission of Co-ordinator/HOD/Director  
RMC\* Remedial classes for weaker and underperformed student