

Devi Ahilya Vishwavidyalaya, Indore
Institute of Engineering & Technology

CLASS TIME TABLE (Session: July- Dec 2025) w.e.f. 01/07/2025

Class & Branch: II Year BE Electronics & Telecommunication (Section-B)						Class Teacher : Mr. Ashish Panchal				
Day	8:00-9:00	9:00-10:00	10:00-11:00	11:00-12:00	12:00-13:00	13:00-14:00	14:00-15:00	15:00-16:00	16:00-17:00	
Monday			3RTPC3 AP E-105	3RTPC3 AP E-105	3RTPC2 BB E-105	B R E A K	3RTPC4 AV E-105	3RTPC4 AV E-105	RMC* 3RTPC2 BB E-105	
Tuesday		3RTPC4 AJ E-105	3RTPC3 AP B2 D-107		3RTPC1 SVT D-101		3RTBS1 SSh E-105	3RTBS1 SSh E-105	RMC* 3RTPC4 AV E-105	
Wednesday		3RTPC5 PSN/SR B1 D-102		3RTPC1 SVT D-101	3RTPC1 SVT D-101		3RTIK1 VF1 E-105	3RTIK1 VF1 E-105	RMC* 3RTPC3 AP E-105	
Thursday		RMC* 3RTBS1 SSh E-105	3RTPC3 AP B1 D-107		RMC* 3RTPC1 SVT D-101		3RTPC2 BB E-105	3RTPC1 PSN/MZA B1 D-104		
Friday		3RTPC5 PSN/SR B2 D-102		3RTPC3 AP E-105	3RTBS1 SSh E-105		3RTPC1 PSN/MZA B2 D-104		3RTPC2 BB E-105	
Saturday							3RTPC5 SS/MJ B1 E-105			
Subject Code	Subject Name		Marks Scheme		Keys.		Faculty Name		Keys.	Faculty Name
3RTBS1	Applied Maths - III		L-2, T-1, P-0		SSh	Mrs.Suman Sharma				
3RTPC1	Digital Electronics		L-2, T-1, P-1		SVT	Dr Sanjiv Tokekar	MZA		Mh. Zafar Ansari	
3RTPC2	Data Structures		L-2, T-1, P-1		BB	Mr. Brahman Bhalavi				
3RTPC3	Analog Electronics		L-2, T-1, P-1		AP	Mr. Ashish Panchal	MJ		Dr Madhvi	
3RTPC4	Signal and system Analysis		L-2, T-1, P-0		AV/AJ	Dr Ajay Verma/Mr. Amit Jha	SR		Ms. Sapna Rai	
3RTPC5	Software Workshop - I		L-0, T-0, P-2		SS/SR	Dr. Sangita Solanki	PSN		Mr Praveen S	
3RTIK1	Indian Knowledge System		L-2, T-0, P-0		VF1	VISITING FACULTY				

Note:- Classes will be conducted only in allotted rooms & time, Mutual exchange of lectures is not permitted until the permission of Co-ordinator/HOD/Director
RMC* Remedial classes for weaker and underperformed student

Prof. I/C

HOD

Director