

Devi Ahilya Vishwavidyalaya, Indore
Institute of Engineering & Technology

CLASS TIME TABLE (Session: Jan. 2024- May. 2024) w.e.f- Dated: 08/01/2024

Class & Branch: II Year BE Electronics & Instrumentation

Class Teacher : DR. SHAILENDRA PATHAK

Day	8:00-9:00	9:00-10:00	10:00-11:00	11:00-12:00	12:00-13:00	13:00-14:00	14:00-15:00	15:00-16:00	16:00-17:00
Monday		4EIRL2 PSN/ PDA D-107 B1		4EIRC1 SKP E-105	4EIRC3 SB E-105	B R E A K	4EIRC2 AJ E-105	4EIRC2 AJ E-105	
		4EIRC3 SB D-108 B2							
Tuesday		4EIRG2 HP E-105	4EIRG2 HP E-105	4EIRC4 VN D- 205 B1			4EIRC4 VN E-105	4EIRC4 VN E-105	
				4EIRC2 AJ D- 209 B2					
Wednesday		4SERS4 E-105 DA	4SERS4 E-105 DA	4EIRC1 SKP E-105	4EIRC3 SB E-105		4EIRC4 VN E-105	4EIRC4 VN E-105	
Thursday		4EIRC2 AJ E-105	4EIRC2 AJ E-105	4EIRG2 HP E-105	4EIRG2 HP E-105		4EIRC1 SKP E-105		
Friday		4EIRL2 PSN /PDA D-107 B2		4EIRC3 SB E-105	4EIRC3 SB E-105		4EIRC1 SKP E-105	4EIRC4 VN D- 205 B2	
		4EIRC3 SB D-108 B1				4EIRC2 AJ D- 209 B1			
Saturday									
Subject Code	Subject Name		Marks Scheme L_T_P		Keys.	Faculty Name		Keys.	Faculty Name
4EIRC1	DIGITAL SIGNAL PROCESSING		3_1_0		SKP	DR. SHAILENDRA PATHAK			
4EIRC2	SENSOR AND TRANSDUCERS		3_1_1		AJ	MR. AMIT JHA			
4EIRC3	LINEAR DEVICES AND APPLICATIONS		3_1_1		SB	DR. SHIVANGI BANDE			
4EIRC4	CMOS DIGITAL DESIGN		3_1_1		VN	DR. VAIBHAV NEEMA			
EIR4G2	ANALOG AND DIGITAL COMM.		3_1_0		HP	DR. HEMLATA PAL			
4EIRL2	Electronic Workshop II		0_0_1		PSN/PDA	MR. PRAVEEN SINGH	DR. P.D.ASHOK		
4SERS4	Engg. Economics		2_0_0		DA	DR. DEEPSHIKHA AGRAWA			

Note:- Classes will be conducted only in allotted rooms & time, Mutual exchange of lectures is not permitted until the permission of Co-ordinator/HOD/Director

RMC* Remedial classes for weaker and underperformed student