

Devi Ahilya Vishwavidyalaya, Indore
Institute of Engineering & Technology

CLASS TIME TABLE (Session: Jan. 2024- May. 2024) w.e.f- Dated: 08/01//2024

Class & Branch: II Year BE Electronics & Telecommunication (Section-A)

Class Teacher :MR. BRAHMAN SINGH BHALAVI

Day	8:00-9:00	9:00-10:00	10:00-11:00	11:00-12:00	12:00-13:00	13:00-14:00	14:00-15:00	15:00-16:00	16:00-17:00
Monday				4ETRG2 SVT D-101	4ETRG2 SVT D-101	B R E A K	4ETRC2 BB D-101	4ETRC2 BB D-101	4ETRC1 DN D-101
Tuesday	4ETRL2 PSN/PDA D-107 B1		4ETRC4 AP D-101	4ETRC4 AP D-101	4ETRC3 SKH D-101		4ETRC3 SKH D-101	4ETRC3 SKH D-101	4ETRC2 BB D-101
	4ETRC4 AP D-209 B2								
Wednesday	4ETRC3 SKH D-108 B1		4ETRG2 SVT D-101	4ETRG2 SVT D-101	4ETRC1 DN D-101		4ETRC1 DN D- 101	4STRS4 D-101 SM	
	4ETRC2 BB D-204 B2								
Thursday	4STRS4 D-101 SM	4STRS4 D-101 SM	4ETRC3 SKH D-101	4ETRC3 SKH D-101	4ETRC2 BB D-101		4ETRC2 BB D-204 B1		
							4ETRC3 SKH D-108 B2		
Friday	4ETRC4 AP D-101		4ETRC4 AP D-101	4ETRC1 DN D-101	4ETRC4 AP D-209 B1				
	4ETRL2 PSN/PDA D-107 B2								
Saturday									
Subject Code	Subject Name	Marks Scheme L_T_P		Keys.	Faculty Name	Keys.	Faculty Name		
4ETRC1	MEARS. SENSORS AND S	3_1_0		DN	DR. DHIRAJ NITNAWARE				
4ETRC2	ELECTROMAGNETI	3_1_1		BB	MR. BRAHMAN SINGH BHAI				
4ETRC3	LINEAR DEVICES AND	3_1_1		SKH	MR. SHAHID KHILJI				
4ETRC4	ANALOG COMM	3_1_1		AP	Mr. ASHISH PANCHAL				
4ETRG2	DIGITAL SIGNAL PROCESSING	3_1_0		SVT	DR. SANJIV TOKEKER				
4ETRL2	Electronic Workshop	0_0_1		PSN/PDA	MR. PARVIN SINGH	DR. P.D. ASHOK			
4STRS4	Engg. Economics	2_0_0		SM	MS. SAVITA MOURYA				

Note:- Classes will be conducted only in allotted rooms & time, Mutual exchange of lectures is not permitted until the permission of Co-ordinator/HOD/Director

RMC* Remedial classes for weaker and underperformed student