

**Devi Ahilya Vishwavidyalaya, Indore**  
**Institute of Engineering & Technology**

**CLASS TIME TABLE (Session: Sep 2020- Dec 2020) w.e.f- Dated: 01/10/2020**

**Class & Branch: IV Year BE Electronics & Telecommunication (Section-A)**

**Class Teacher :Mr Shailendra Pathak**

Day	8:00-9:00	9:00-10:00	10:00-11:00	11:00-12:00	12:00-13:00	13:00-14:00	14:00-15:00	15:00-16:00	16:00-17:00
Monday			ETR7C2 SM		<b>B R E A K</b>	ETR7E1 RS		ETR7E1(B1 )RS ETR7C4(B2) VF	
Tuesday			ETR7C4 SHP			ETR7C3 PDA		ETR7C3(B1 ) PDA	
Wednesday			ETR7C2 SM			ETR7E1 RS		ETR7E1(B2 )RS ETR7C4(B1) SHP	
Thursday			ETR7C4 SHP			ETR7C3 PDA		ETR7C3(B2 ) PDA	
Friday									
Saturday						ETR7P1 (RESPECTIVE GUIDES)			
<b>Subject Code</b>	<b>Subject Name</b>		<b>Marks Scheme</b>		<b>Keys.</b>	<b>Faculty Name</b>			
ETR7C2	OPERATING SYSTEM		L-3: T-1: P-0 = 4 Credits		SM	MR.SHYAM MAHESHWARI			
ETR7C3	ANTENNA & WAVE PROPAGATION		L-3: T-1: P-1 = 4+1 Credits		PDA	DR. P.D ASHOK			
ETR7C4	RF AND MICROWAVE ENGG.		L-3: T-1: P-1 = 4+1 Credits		SHP	MR. SHAIENDRA PATHAK			
ETR7E1	CIRCUIT DESIGN USING HDL		L-3: T-1: P-1 = 4+1 Credits		RS	DR RAVI SINDAL			
ETR7P1	PROJECT PHASE-I								

Note:- Classes will be conducted Online in allotted time, Mutual exchange of lectures is not permitted until the permission of Co-ordinator/HOD/Director